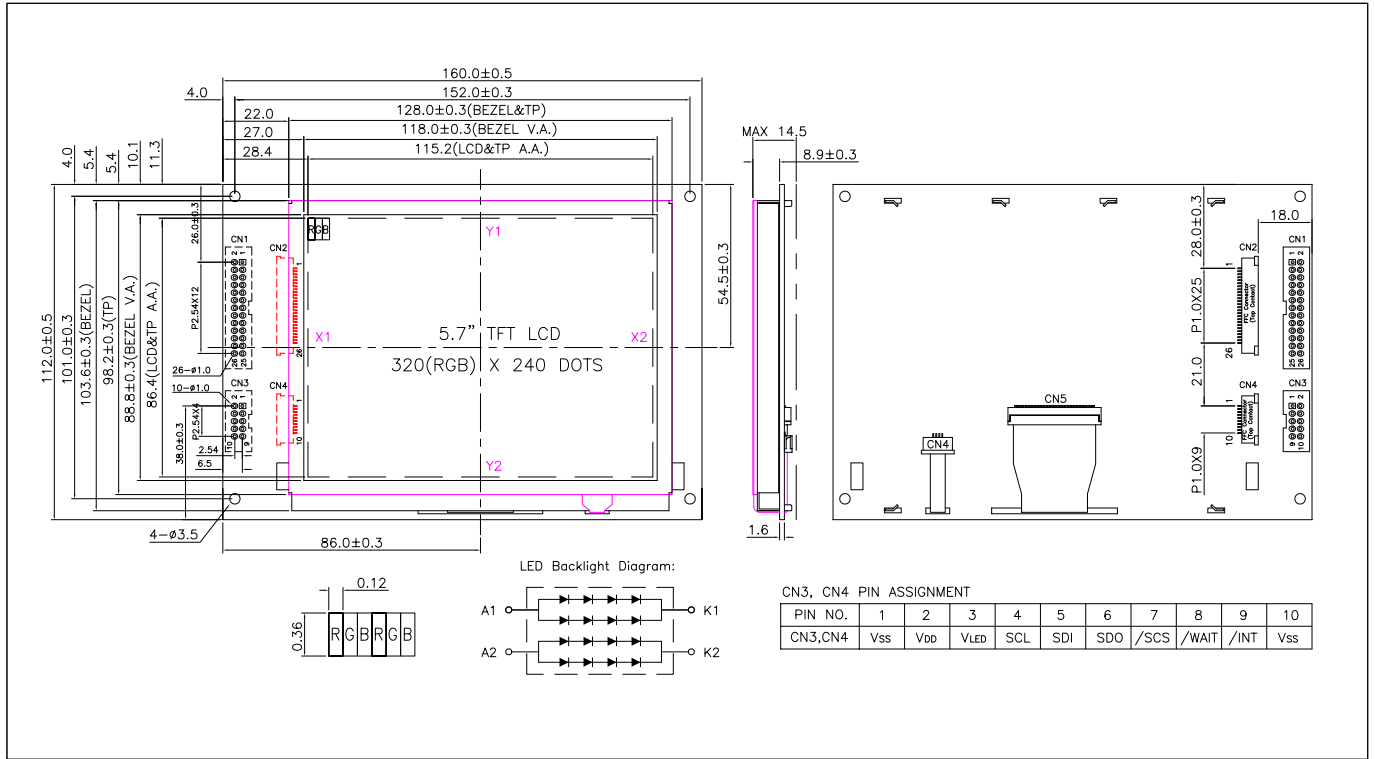


LT057D-33AT

5.7" 320(RGB) x 240 TFT with MCU interface & touch panel, VDD=3.3V



ABSOLUTE MAXIMUM RATINGS

| Item | Symbol | Min. | Max. | Unit |
|-----------------------|------------------------------------|------|-----------------------|------|
| Supply Voltage(Logic) | V _{DD} - V _{SS} | -0.3 | 3.6 | V |
| Supply Voltage(LED) | V _{LED} - V _{SS} | -0.3 | 6.0 | V |
| Input Voltage | V _I | -0.3 | V _{DD} + 0.3 | V |
| Operating Temp. | T _{opr} | -20 | 70 | °C |
| Storage Temp. | T _{stg} | -30 | 80 | °C |

MECHANICAL DATA

| Item | Nominal Dimensions | Unit |
|---------------------------|----------------------|------|
| Module Size (W x H x T) | 160.0 x 112.0 x 14.5 | mm |
| Viewing Area (W x H) | 118.0 x 88.8 | mm |
| Active Area (W x H) | 115.2 x 86.4 | mm |
| Dot Pitch (W x H) | 0.12 x 0.36 | mm |
| Weight | Approx. 270 | g |

ELECTRICAL CHARACTERISTICS (V_{DD}=3.3V±0.3V)

| Item | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|------------------------|------------------|-------------------------|----------------------|------|--------------------|------|
| Input High Voltage | V _{IH} | -- | 0.8V _{DD} | -- | V _{DD} | V |
| Input Low Voltage | V _{IL} | -- | 0 | -- | 0.2V _{DD} | V |
| Output High Voltage | V _{OH} | -- | V _{DD} -0.4 | -- | V _{DD} | V |
| Output Low Voltage | V _{OL} | -- | 0 | -- | 0.4 | V |
| Supply Current (Logic) | I _{DD} | V _{DD} = 3.3V | -- | 100 | 130 | mA |
| Supply Current (LED) | I _{LED} | V _{LED} = 3.3V | -- | 260 | 300 | mA |
| | | V _{LED} = 5.0V | -- | 160 | 200 | mA |

PIN CONNECTIONS (CN1/CN2)

| Pin | Symbol | Level | Function |
|-------|-------------|---------|--|
| 1 | VSS | 0V | GND |
| 2 | VDD | 3.3V | Power supply |
| 3 | VLED | 3.3V-5V | Power supply for LED B/L drivers |
| 4 | RS | H/L | Data or command selection H: Command L: Display data |
| 5 | /WR (R/W) | H/L | Write signal for 8080 MCU. R/W signal for 6800 MCU. |
| 6 | /RD (E) | H/L | Read signal for 8080 MCU. Enable signal for 6800 MCU. |
| 7 | /CS | L | Chip selection signal. Active "L". |
| 8 | /RST | L | Reset signal. Active "L". |
| 9 | /WAIT | L | Wait signal output. Active "L". |
| 10 | /INT | L | Interrupt signal output. Active "L". |
| 11 | DB0 | H/L | Data bus for 8-bit data bus mode. Low order data bus for 16-bit data bus mode. |
| 12 | DB1 | H/L | |
| 13 | DB2 | H/L | |
| 14 | DB3 | H/L | |
| 15 | DB4 | H/L | |
| 16 | DB5 | H/L | |
| 17 | DB6 | H/L | |
| 18 | DB7 | H/L | |
| 19-26 | DB8 to DB15 | H/L | High order data bus for 16-bit data bus mode. Keep DB8 to DB15 open when 8-bit data bus mode is used. |

BLOCK DIAGRAM

